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INFORMAL DRAWINGS
FOR
UNITED STATES LETTERS PATENT

TITLE: DATA STORAGE SYSTEM HAVING SEPARATE DATA TRANSFER SECTION AND MESSAGE NETWORK

APPLICANT: YUVAL OFEK, DAVID L. BLACK,
STEPHEN D. MACARTHUR, RICHARD WHEELER AND
ROBERT THIBAULT

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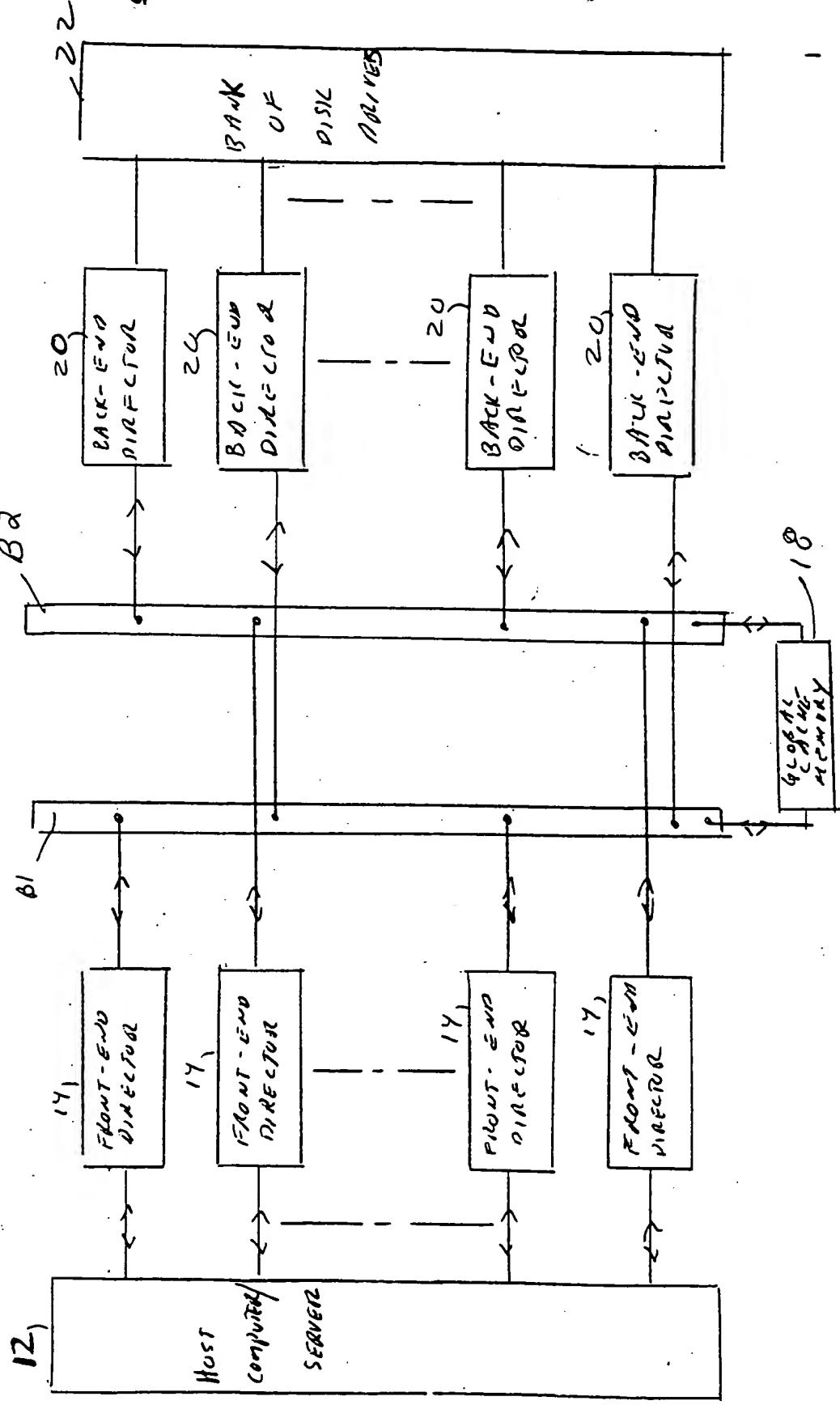
Signature

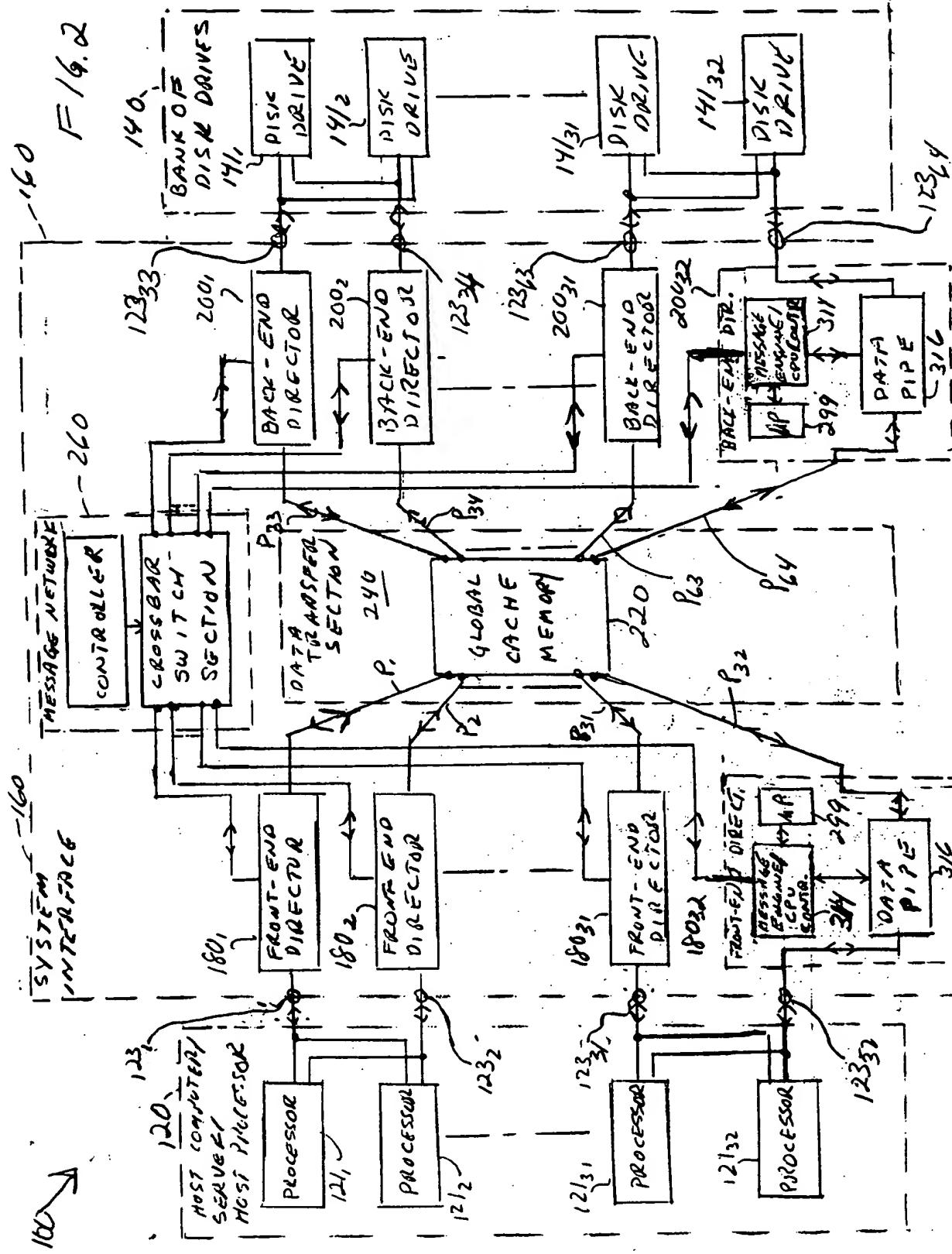
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FIG.1
DRAWA NAR





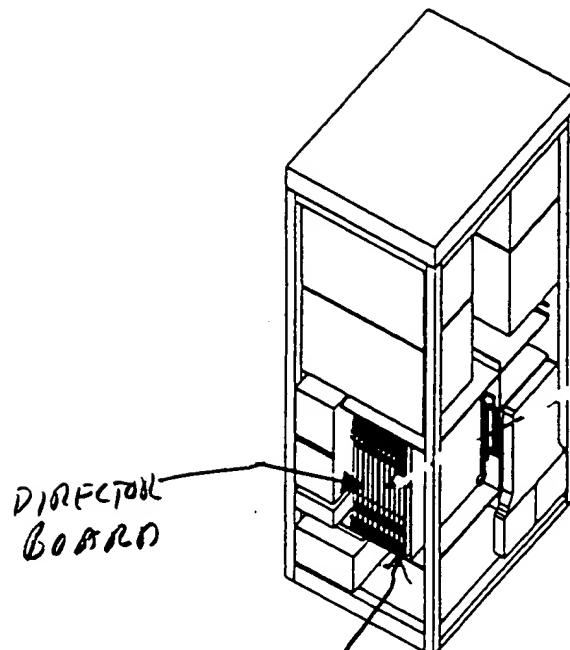
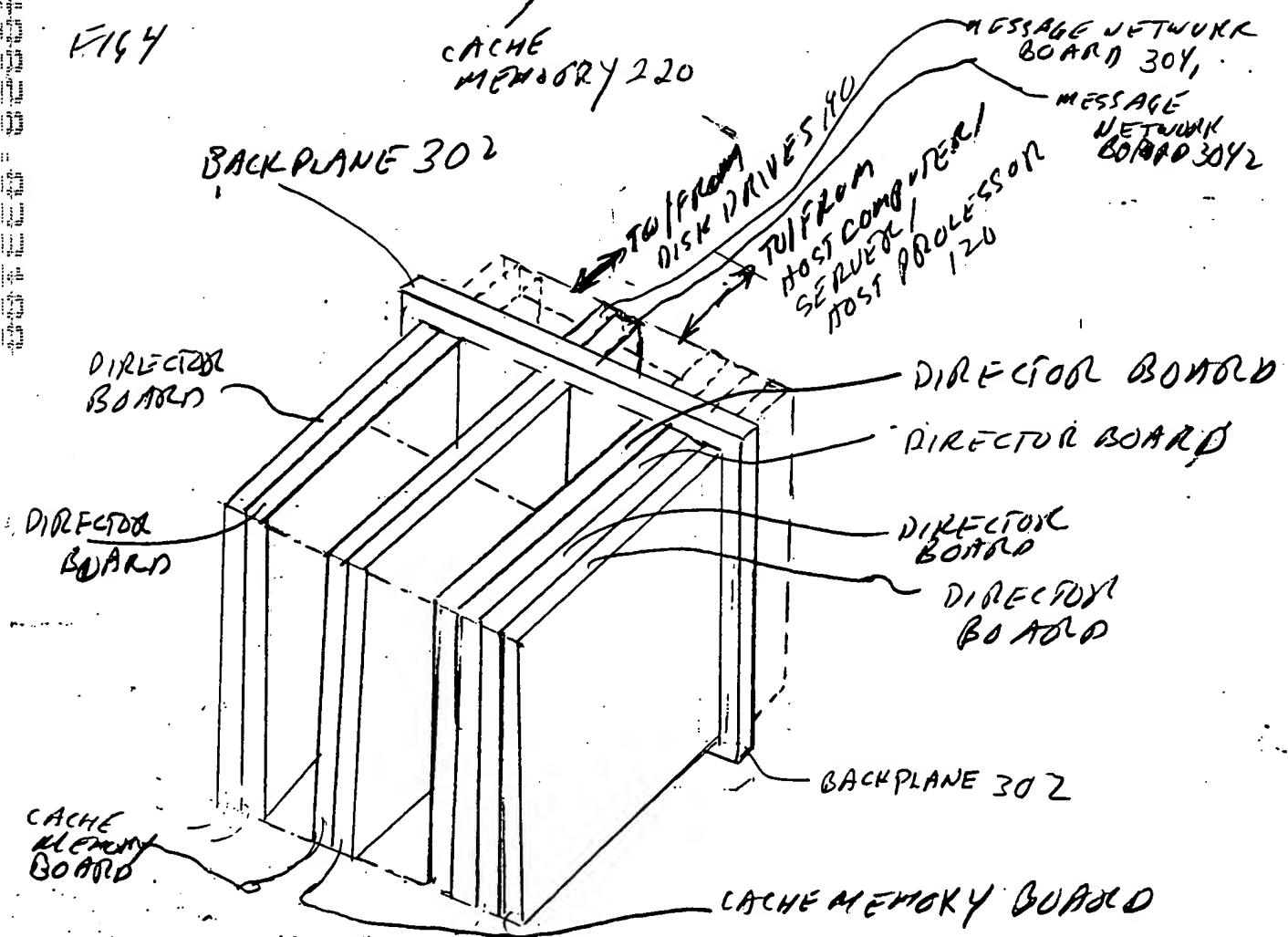
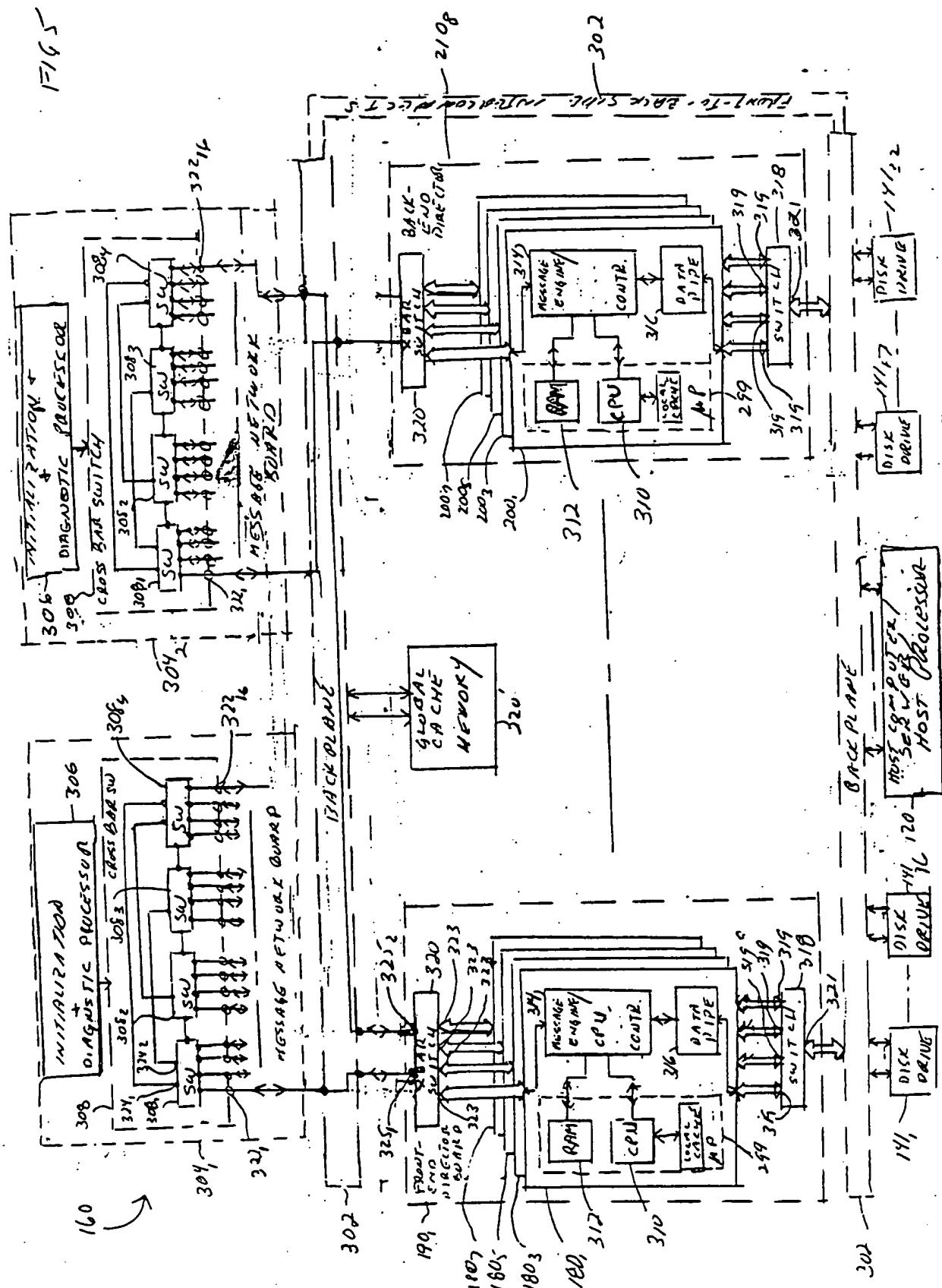
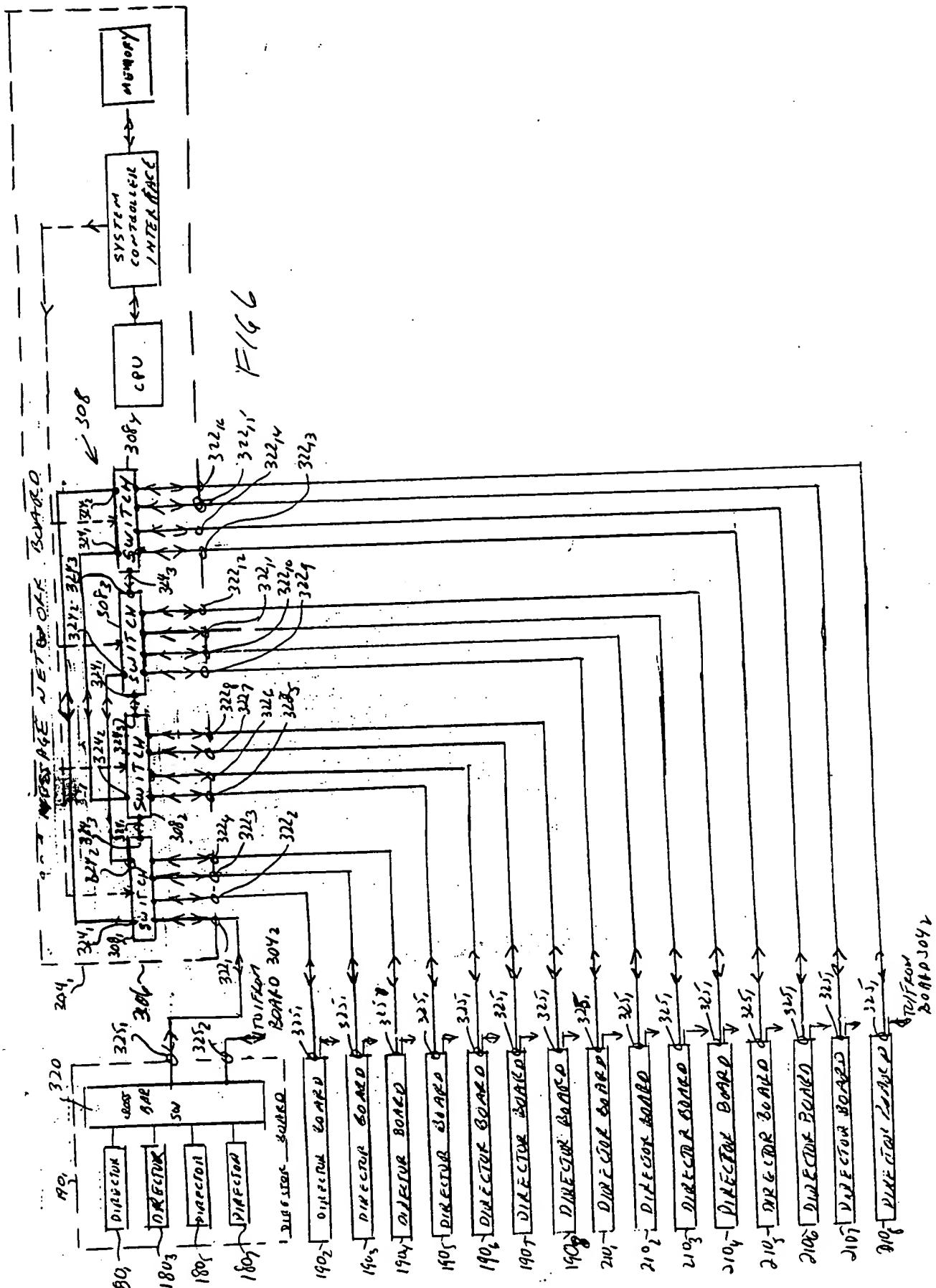


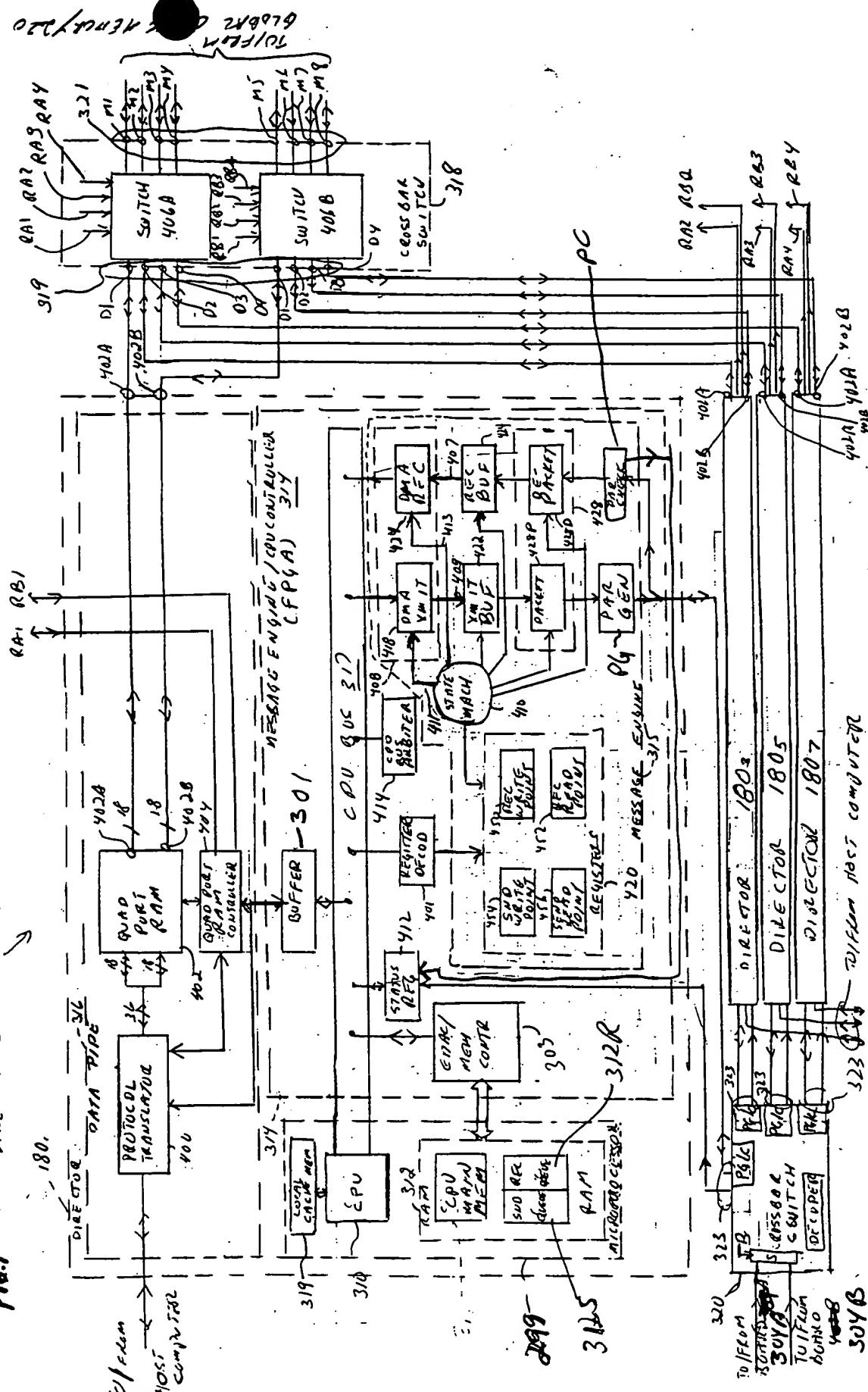
FIG. 3







File 7 DIRECTOR BOARDED 1801



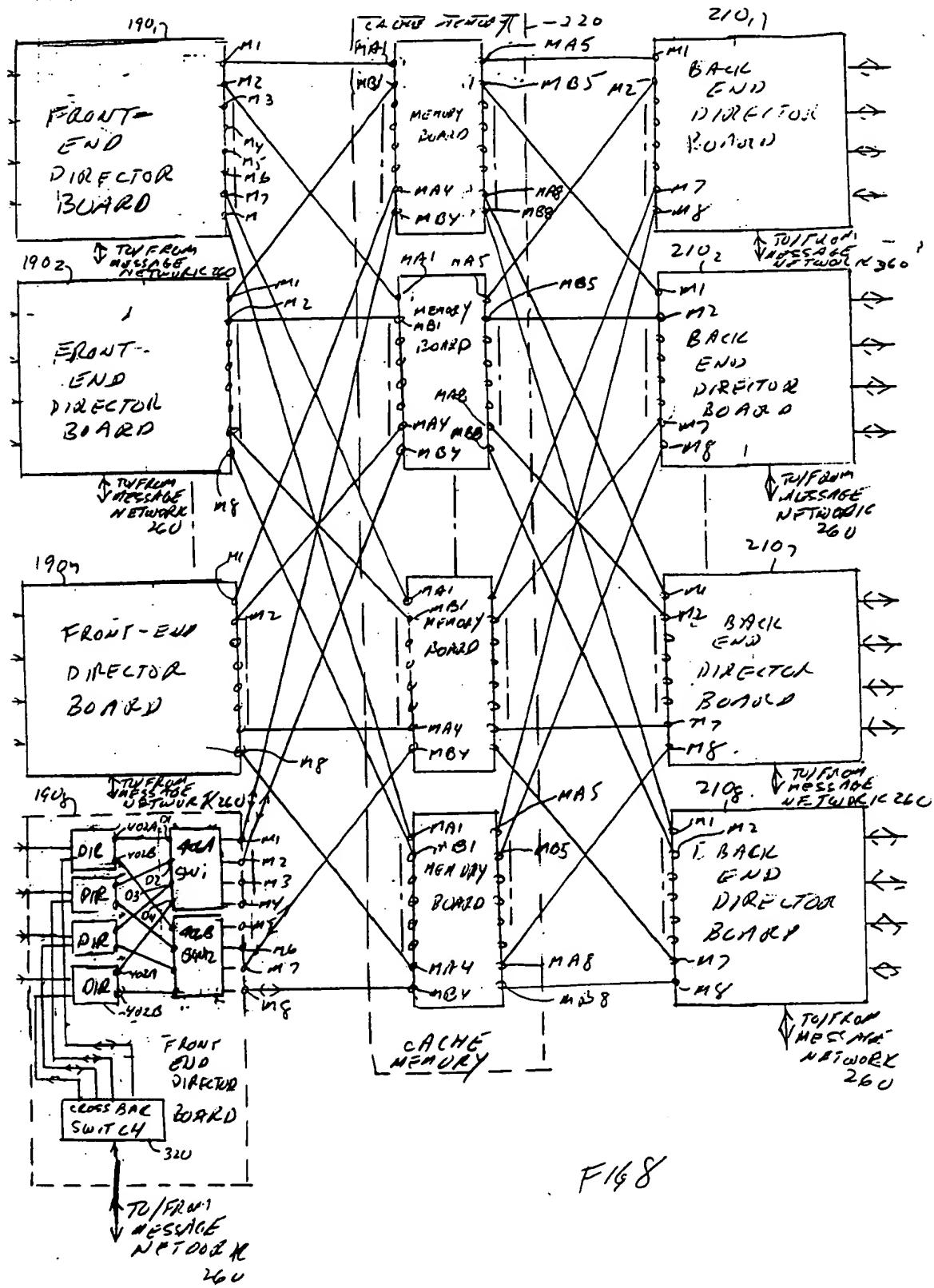
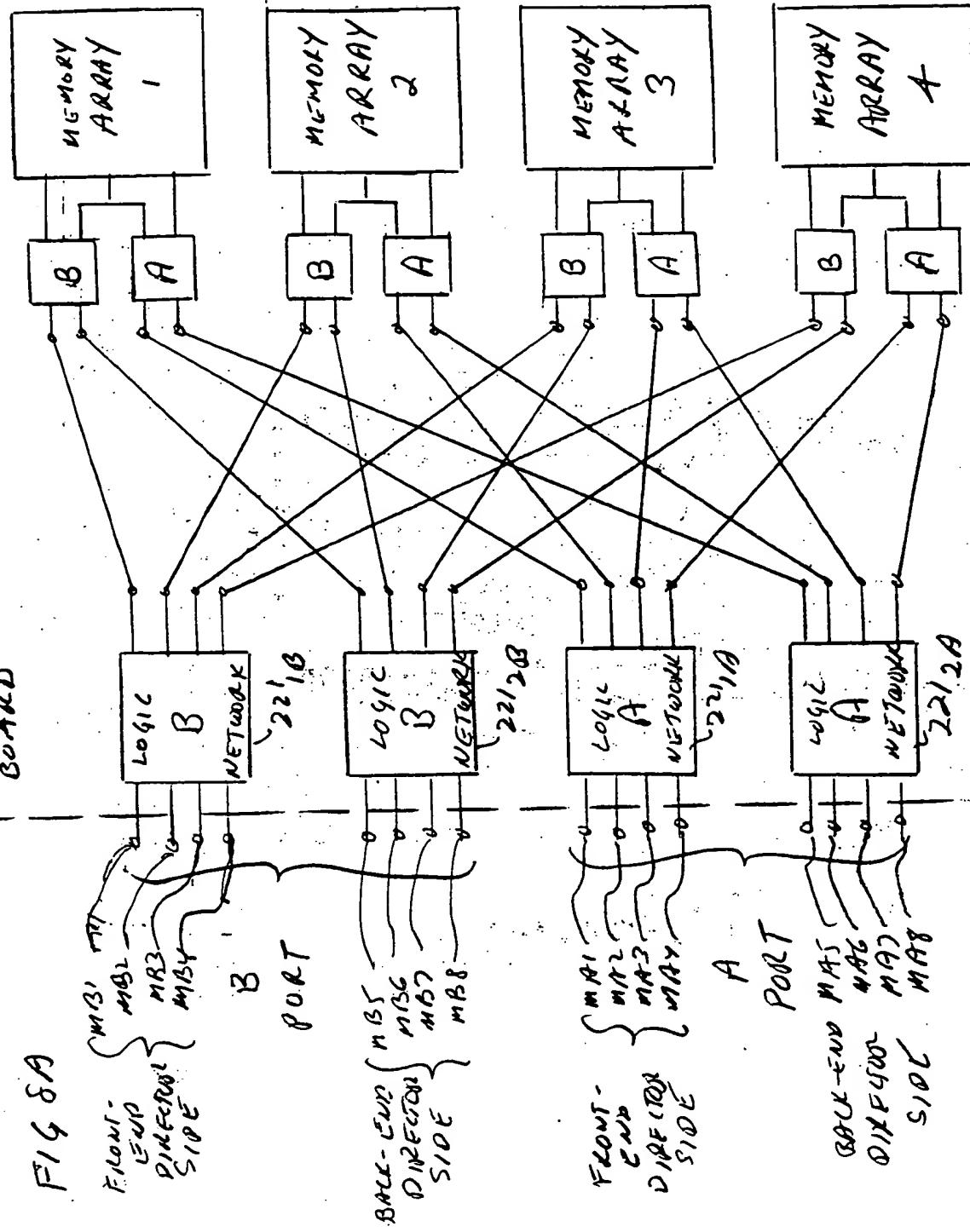


FIG 8

FIG 8A

22/23
MEMORY BOARD



F11 8B

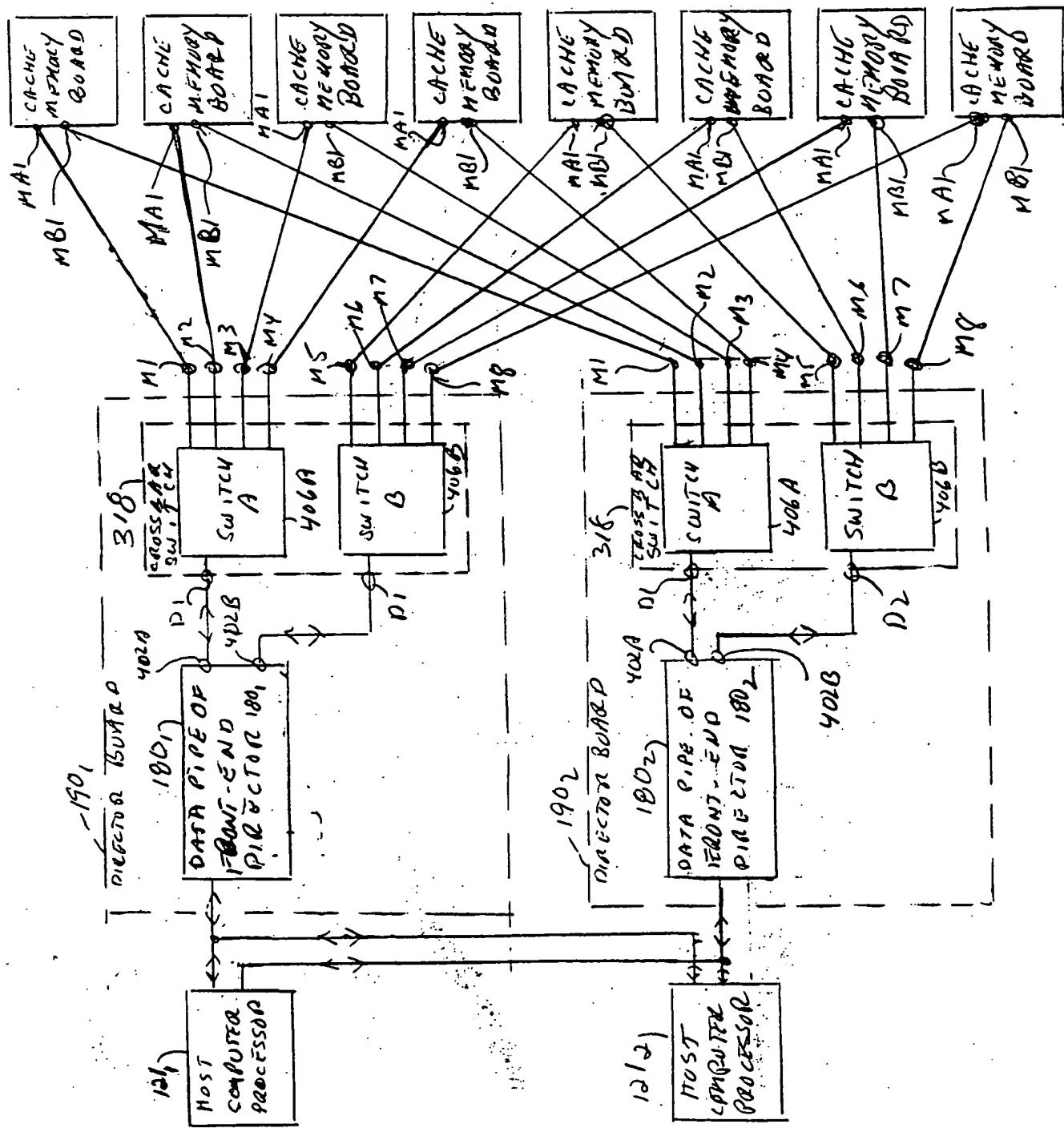


FIG. 8C

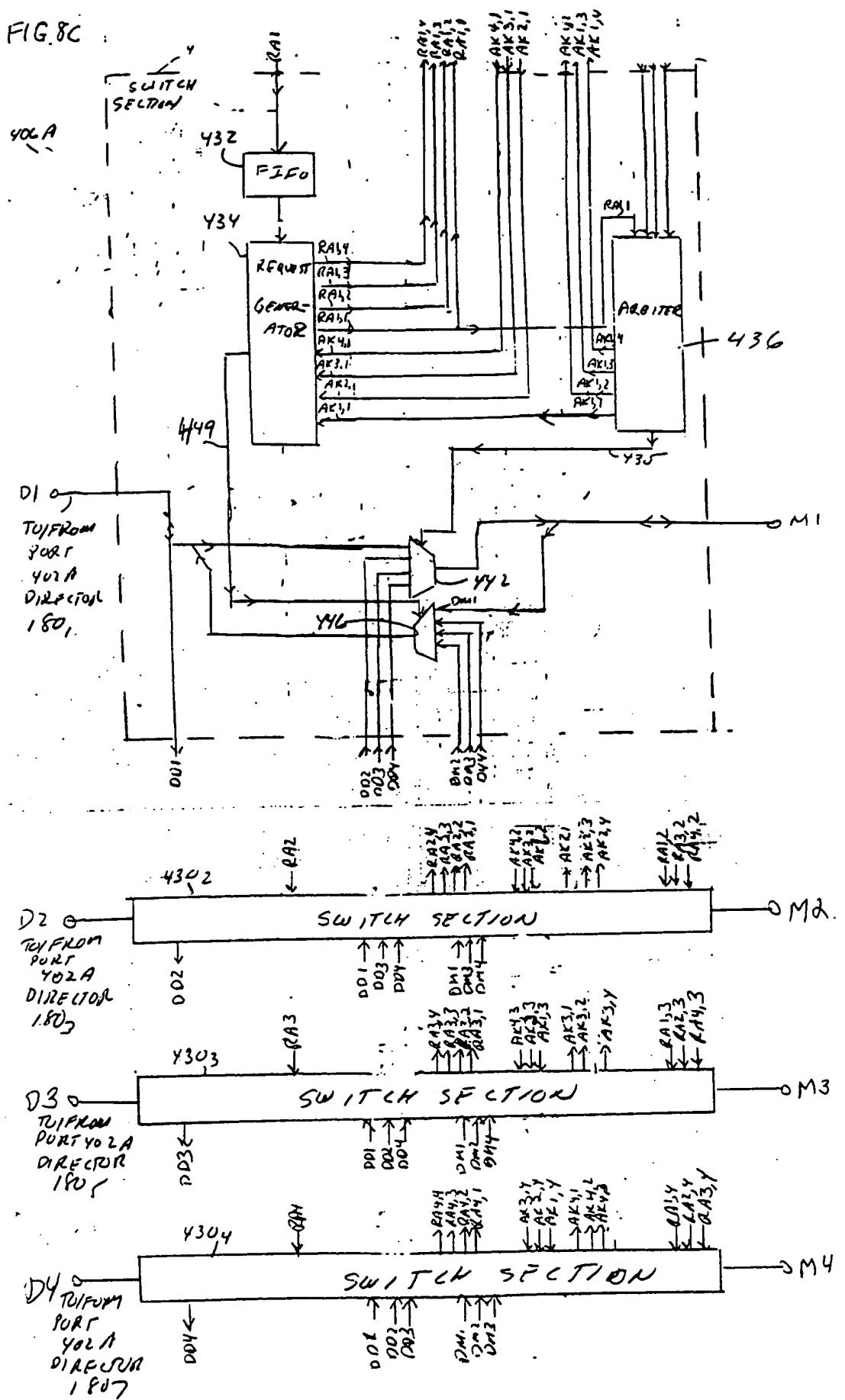


FIG 9

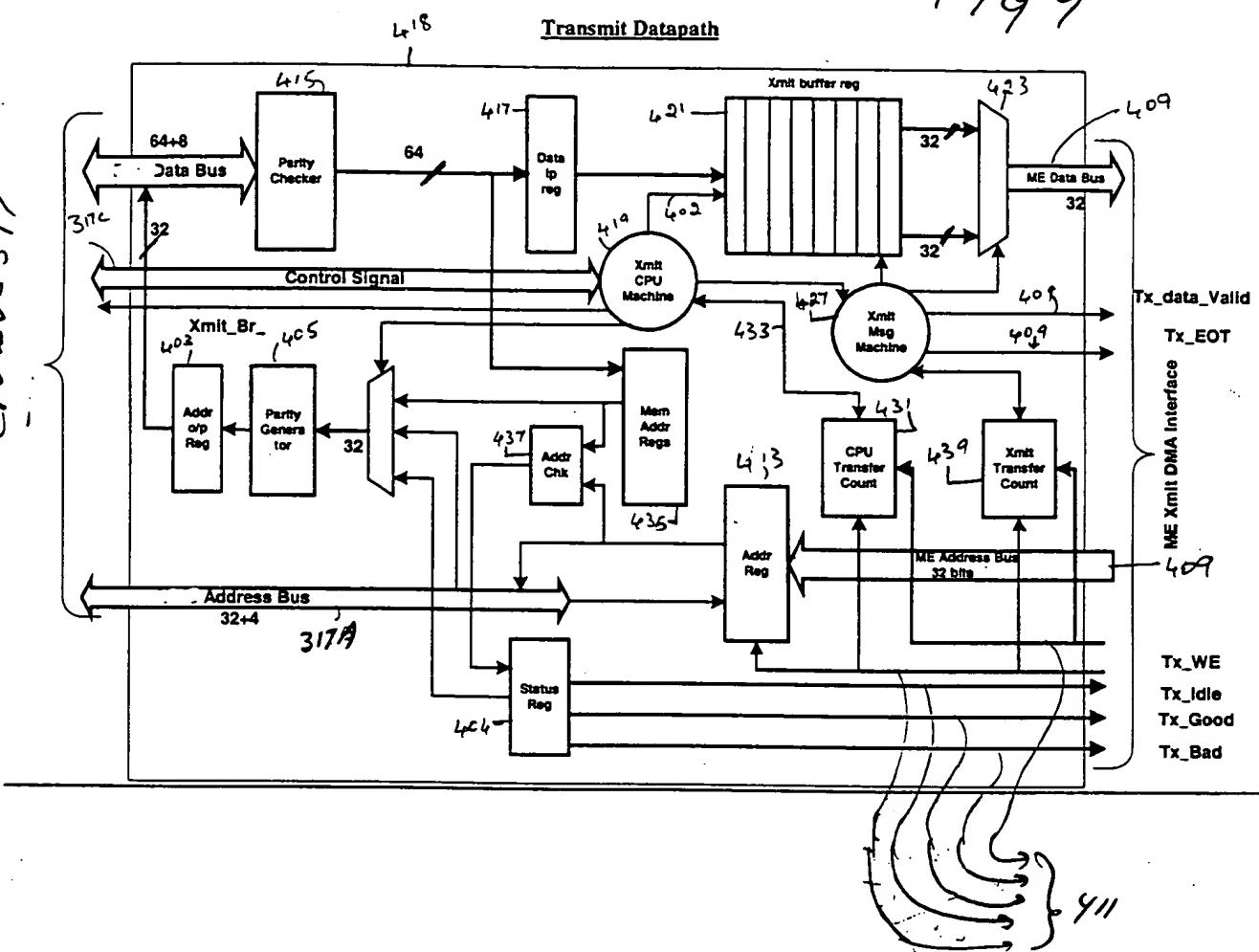
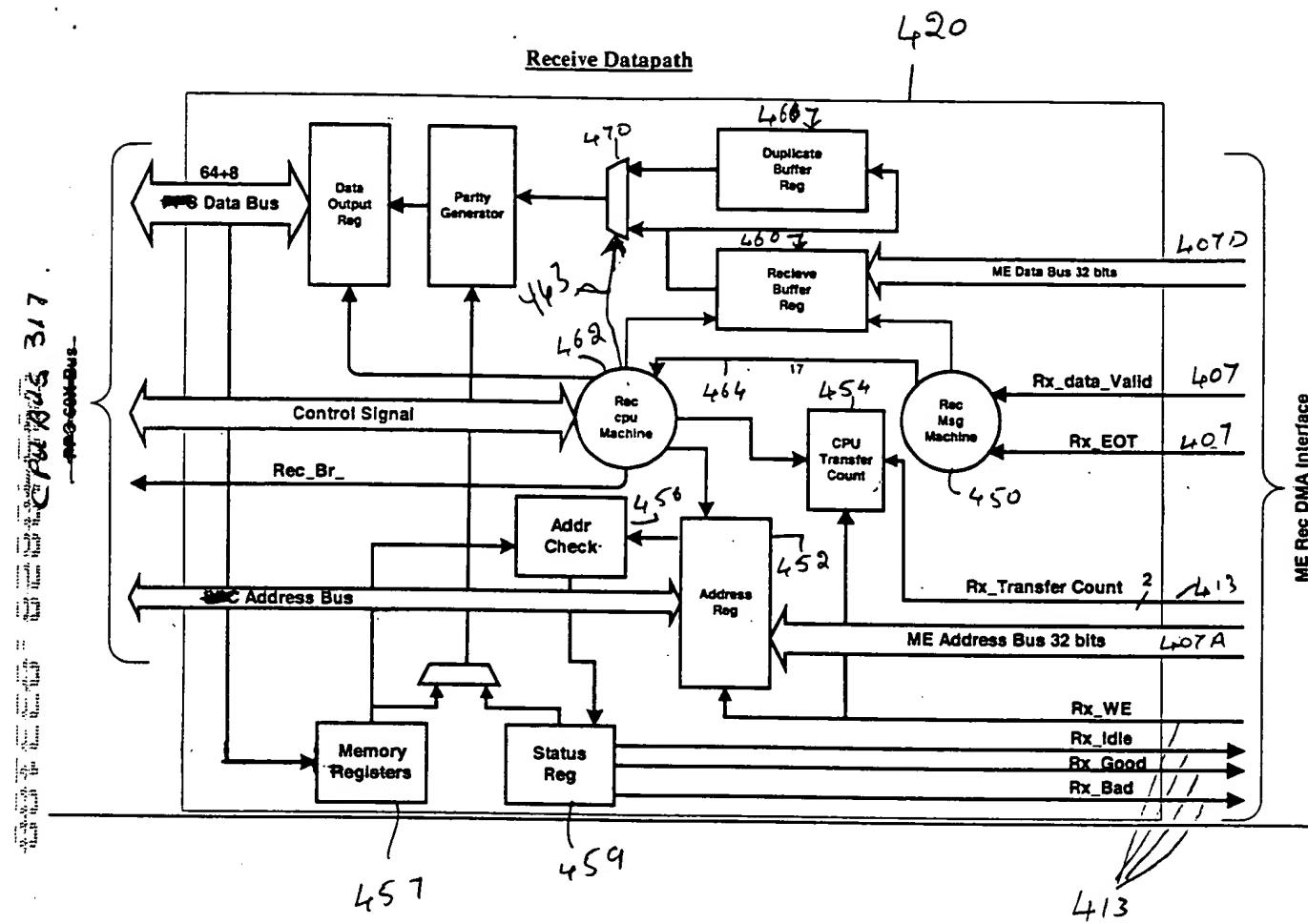


FIG 10



Message Bus Send Operation

FIG. 11A

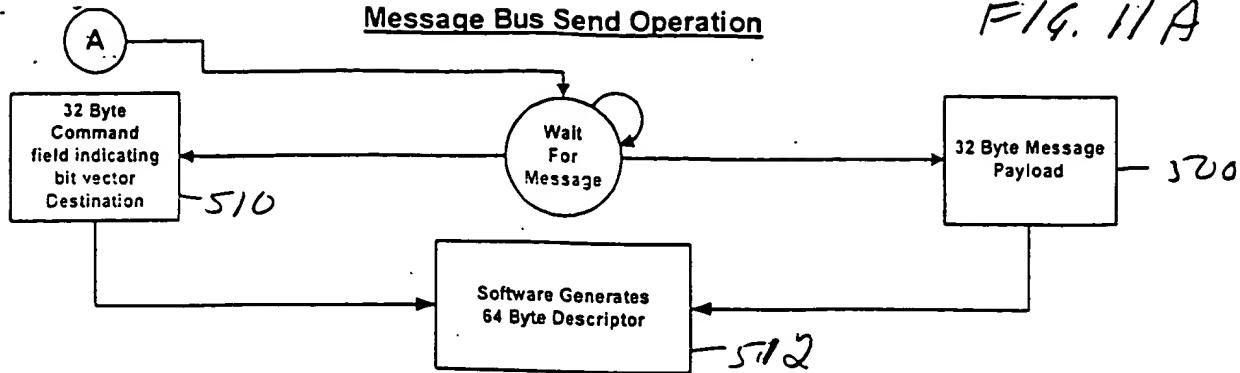
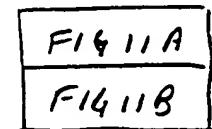
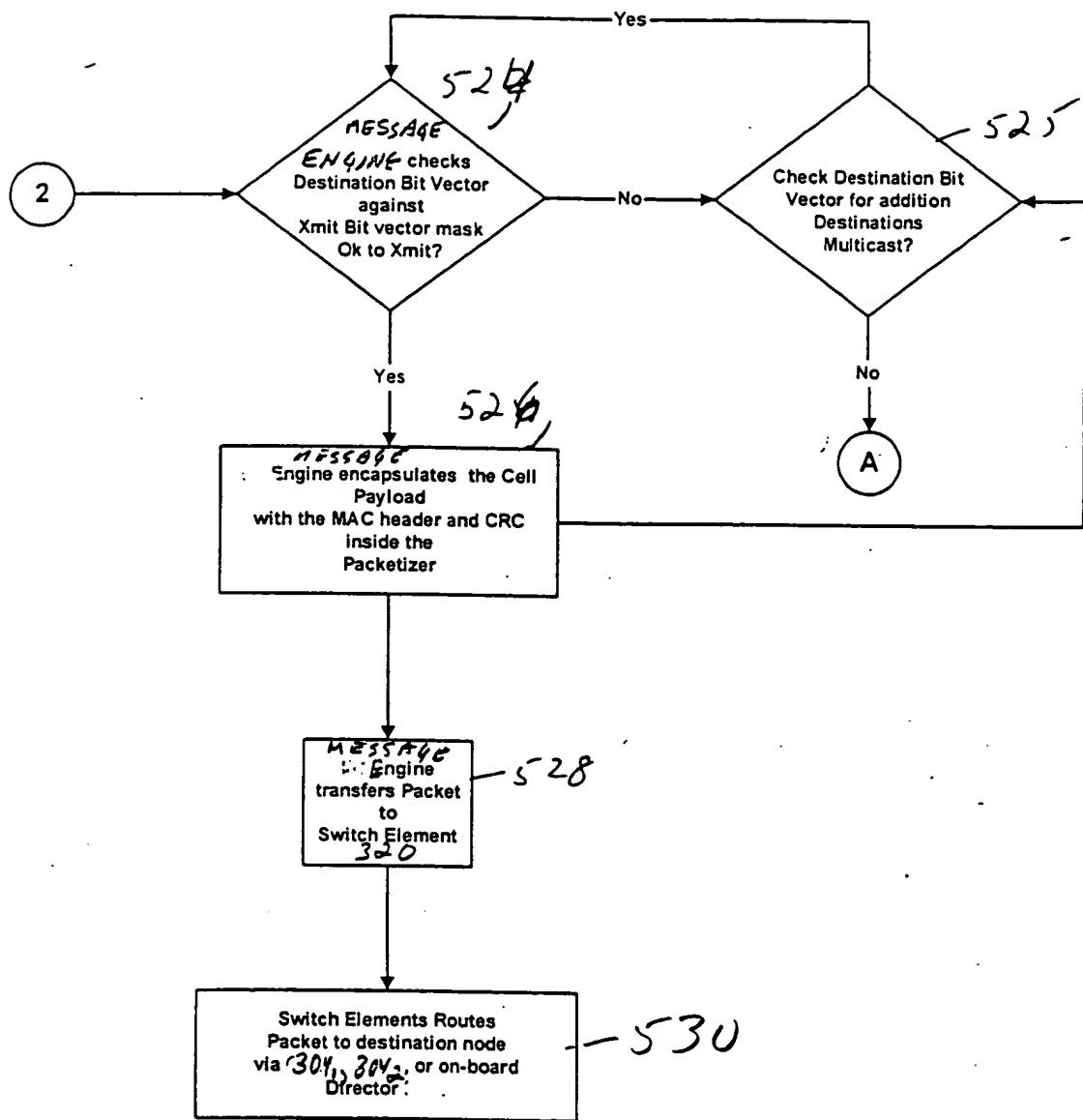


FIG 11



Message Bus Send Operation Continued

FIG 11B



BIT POSITION

1	2	3	4	5	6	7	8
1	0	0	0	0	0	0	0

FIG 11C

BIT POSITION

1	2	3	4	5	6	7	8
0	1	0	0	0	0	0	0

FIG 11D

BIT POSITION

1	2	3	4	5	6	7	8
0	1	1	0	0	0	1	1

FIG 11E

BIT POSITION

1	2	3	4	5	6	7	8
1	1	1	1	0	0	1	0

FIG 11F

BIT POSITION

1	2	3	4	5	6	7	8
0	1	1	0	0	0	1	0

FIG 11G

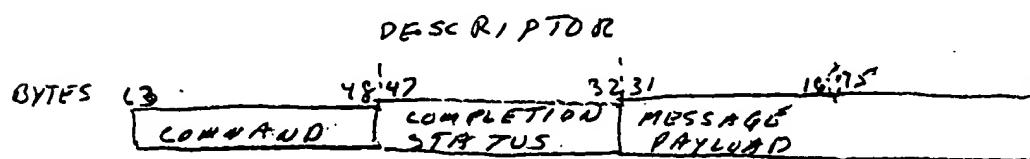


FIG 2A

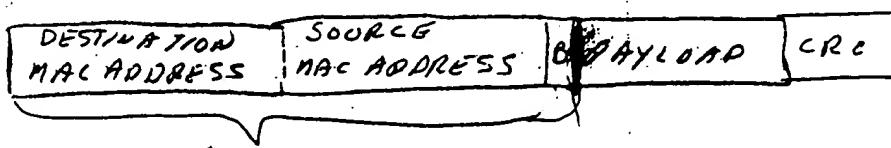
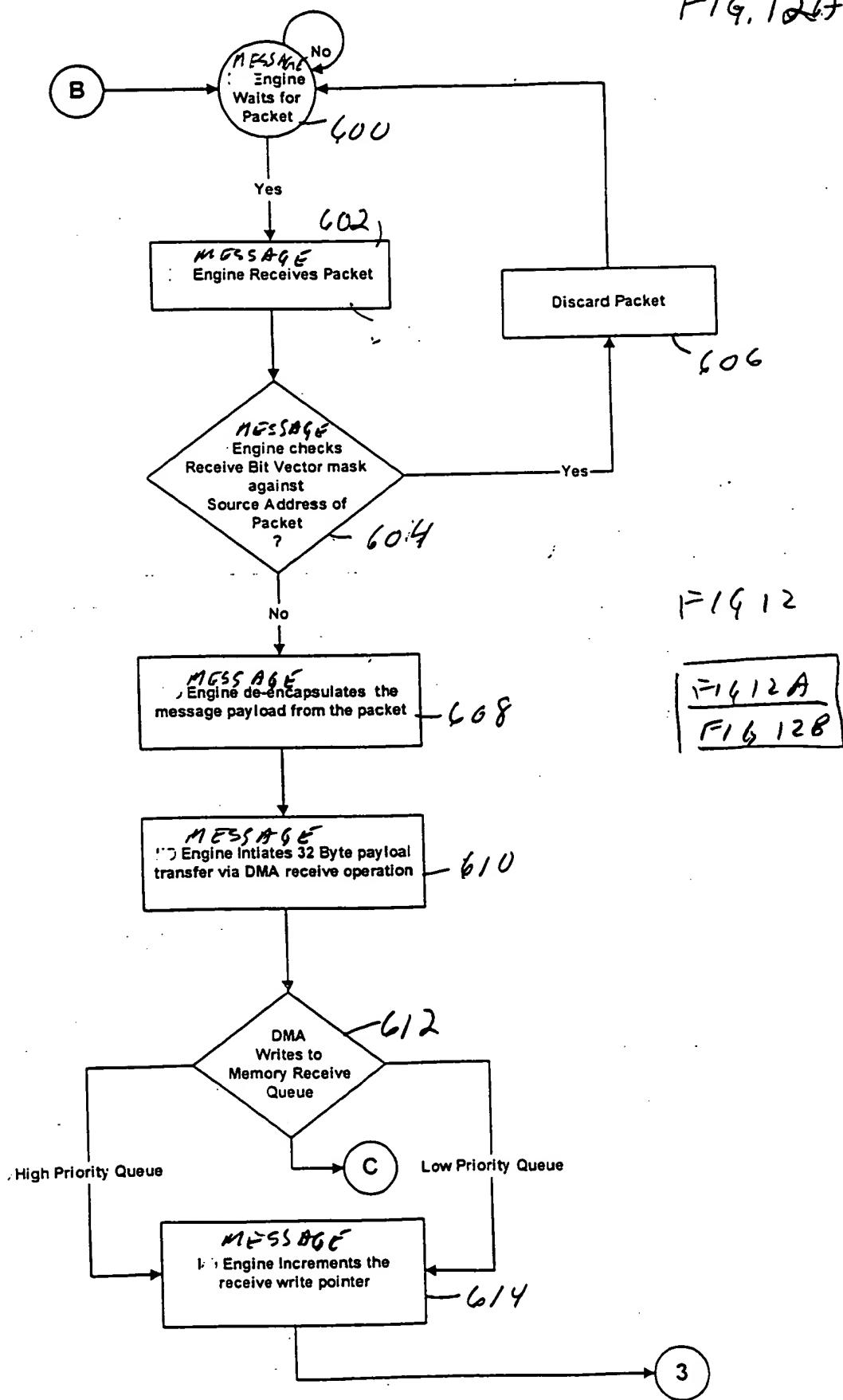


FIG 2B

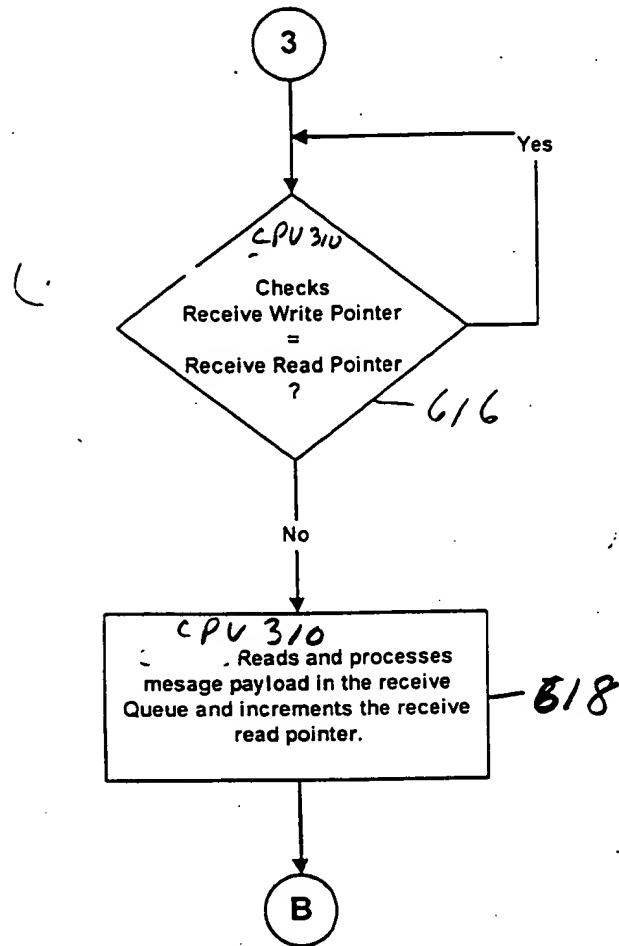
MAC HEADER

Message Bus Receive Operation

FIG. 12A



Message Bus Receive Operation Continued



F-14 12B

616

- 618

Message Bus Acknowledgement Operation

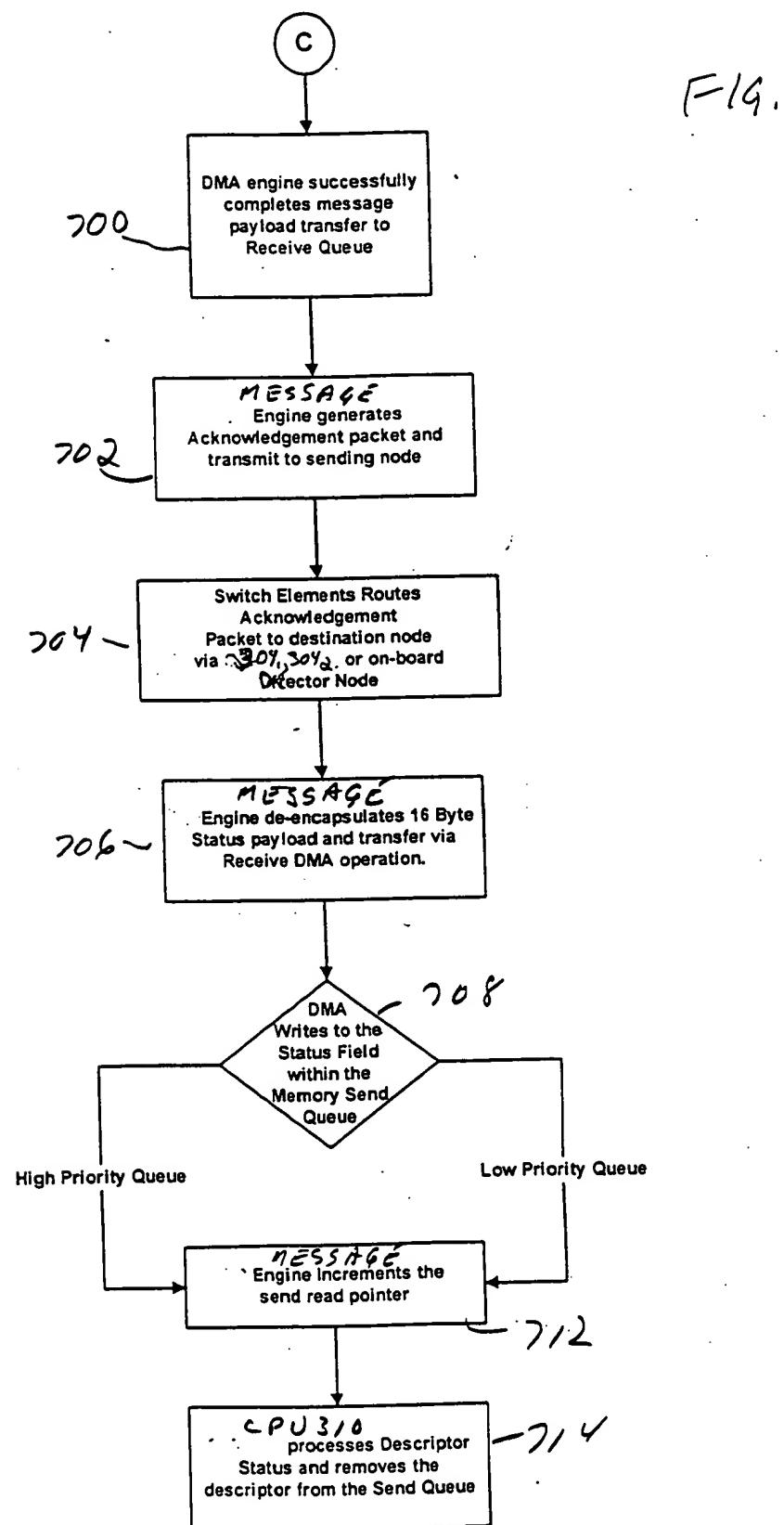
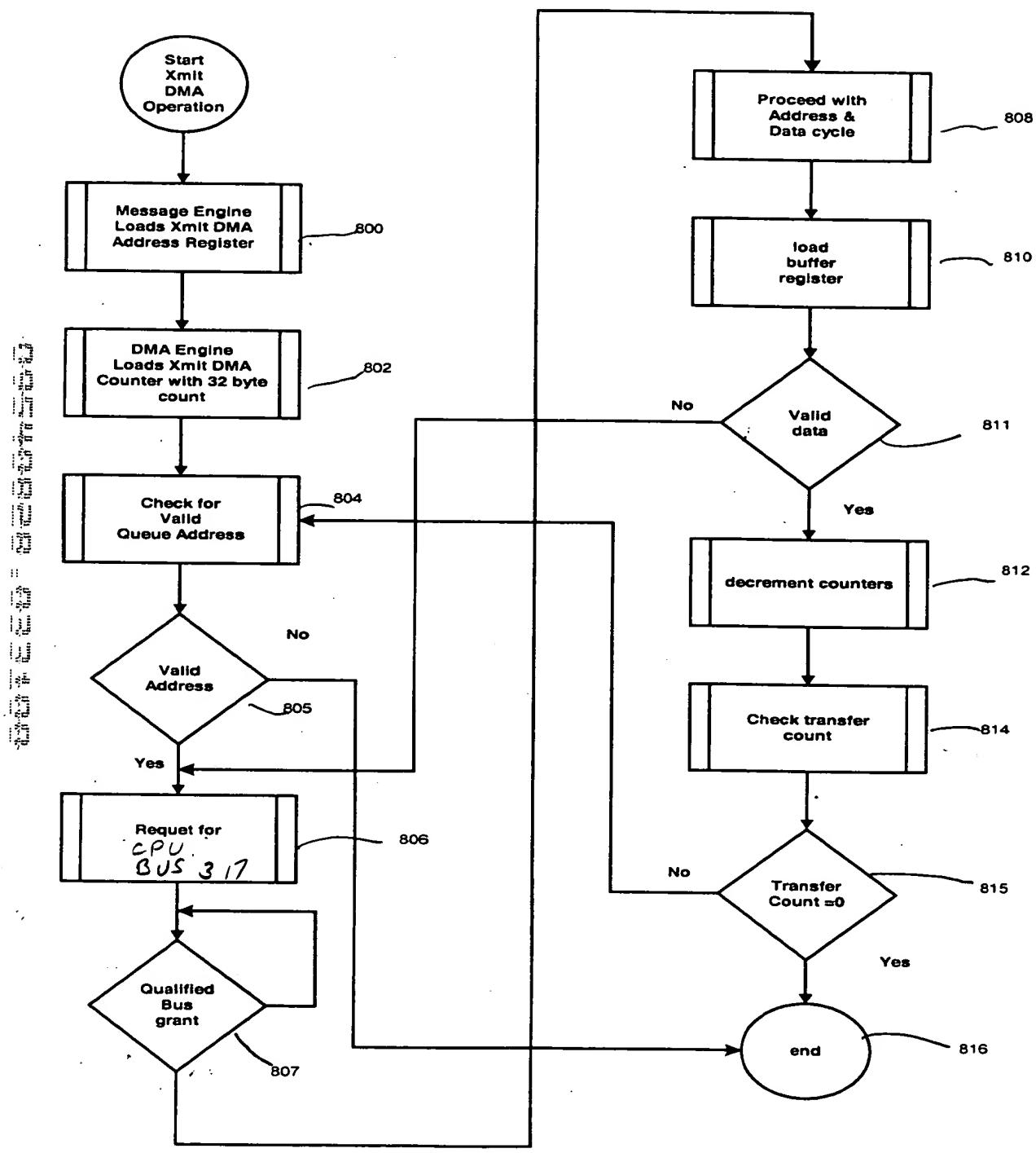


FIG 1AA

Xmit CPU flow



F-1414B

Xmit Msg flow

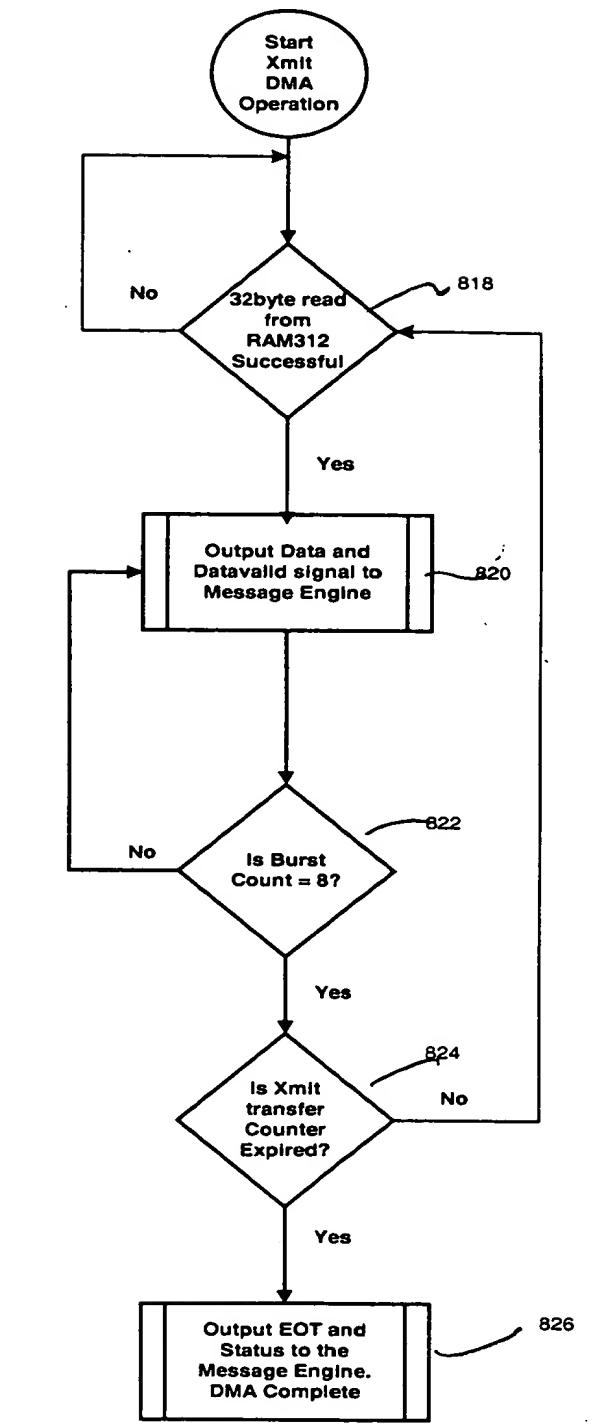
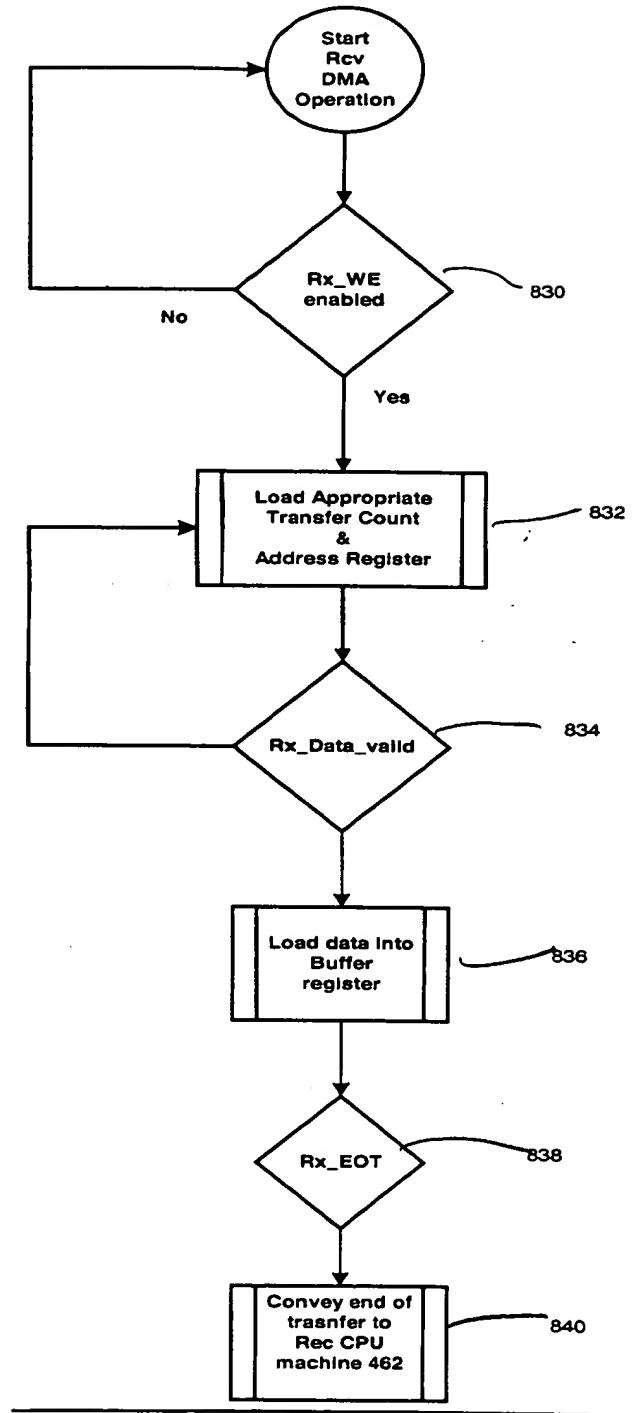


FIG 15A

Rec msg flow



P1615B

Rec cpu flow

